Atty. Docket No.: 3175-Z

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of Serial No. 10/086,214

Examiner James C. Kerveros

Filed: March 27, 2002 Art Unit 2133

For: Method and Apparatus for Diagnosing Failures in an Integrated

Circuit Using Design-for-Debug (DFD) Techniques

NOTICE OF APPEAL FROM THE PRIMARY EXAMINER TO THE BOARD OF PATENT APPEALS AND INTERFERENCES

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Applicant(s) hereby appeal(s) to the Board of Patent Appeals and Interferences from the decision dated October 5, 2005 of the Primary Examiner finally rejecting Claims 85 - 103.

The Appeal fee of \$250.00 is included in the attached check:

XX Enclosed: Check

 Not	required		(fee	paid	in	prior	appeal	in	this	application
with	no	Board	deci	sion	on _¥ t	he mer	its.)			

Petition is hereby made to the Commissioner of Patents and Trademarks to extend the period for filing this Notice of Appeal for one month, so as to expire _____. The fee for this extension in the amount of \$____ is included in the attached check.

The Commissioner is hereby authorized to charge any additional fees which may be required (or credit any overpayment) to Deposit Account No. 26-0090.

Respectfully submitted,

Jim Zegeen

Jim Zegeer, Red No. 18 65 74 10086214 Attorney for Applicants 62 FC:2401 250.00 OP

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Date: February 17, 2006

In the event this paper is deemed not timely filed, the applicant hereby petitions for an appropriate extension of time. The fee for this extension may be charged to Deposit Account No. 26-0090 along with any other additional fees which may be required with respect to this paper.